

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q. A.

APPLICATION NO. 09/660198	CONT/PRIOR F	CLASS 257	SUBCLASS 347	ART UNIT 2811	EXAMINER Lolce Menas
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TS Keiko Kawamura

APPLICANTS

TITLE

Transistor capable reducing on-resistance and improving withstand voltage and method of manufacturing the same

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE		
			Amount Due	Date Paid	
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